

# Dedicated Line Access Switch Debugging



## Overview

This article gives a practical, high-signal overview of JTAG and SWD debugging techniques—from wiring and level shifting to breakpoints, trace, scripting with OpenOCD/pyOCD, and design tips that make production and service smoother. To enable debugging of the Ethernet management port, use the debug fastethernet command in EXEC mode. If your pcb design will ship in volume, the details below will pay. JTAG (named after the Joint Test Action Group which codified it) is an industry standard for verifying designs of and testing printed circuit boards after manufacture. JTAG implements standards for on-chip instrumentation in electronic design automation (EDA) as a complementary tool to digital. OpenOCD is a powerful tool for On-Chip Debugging of ARM, MIPS, and some other architectures. The appropriate configuration file (make this a link to the file) should look like: To use it with openocd: Boundary scan can be used to take control of a device to set I/O pin state (EXTEST), or to view. JTAG/Boundary-Scan Technology for PCB Testing and In-System Configuration is an essential technique widely used in the production of electronic assemblies in the 21st century.



## Article Content

### DGS-1510 Series Gigabit Ethernet SmartPro Switch CLI Reference

After the Switch was rebooted, or when the users logs out and back in, the newly created username and password must be entered to access the CLI interface again, as seen below.

### A Deep Dive into the ARM Debug Access Port

The ARM Debug Access Port (DAP) is a crucial component of the ARM debugging architecture that allows developers to gain access to and

### Interface and Hardware Components Command Reference, Cisco

To enable debugging on a stack member, you can start a session from the stack master by using the session switch-number EXEC command. Then enter the debug command at the

### AURIX Training Debug Support

Highlights Debug support is based on two components: OCDS (On-Chip Debug System) and MCDS (Multi Core Debug Solution), which offer debugging and performance optimization for the software

### Training JTAG Interface

For most embedded CPU architecture implementations, the JTAG port is used by the debugger to interface the chip for debugging one or more cores.

### Understand Important Information on Debug Commands

This document describes general guidelines on using debug commands including the debug ip packet command available on Cisco IOS® platforms.

### JTAG Interface Training: Debugging & Implementation

Learn JTAG interface basics, implementation, and custom access for debugging embedded systems. A comprehensive training manual for college/university level.

### Training JTAG Interface: Debugging & Custom Access

Learn JTAG interface debugging, basics, implementation, and custom access with this training manual. Ideal for embedded systems engineers.

### Beyond breakpoints: Advanced debugging for RISC-V

Picture 1. Example of a high-level-language debugger for RISC-V With a closer look, it's possible to observe that a high-level-language debugger allows to

### Remote Development Tips and Tricks

Visual Studio Code Remote Development troubleshooting tips and tricks for SSH, Containers, and the Windows Subsystem for Linux (WSL)

Troubleshoot Link Aggregation Control Protocol (LACP)

This document describes how to troubleshoot Link Aggregation Control Protocol (LACP) on Nexus 9000 cloudscale family.

Debug Command on CISCO Router/Switch

Command Debug Use This is a very useful but sometimes dangerous command. It allows you to see how the router or switch processes many different protocols. If your not careful you can

IN-SYSTEM DEVICE PROGRAMMING GUIDE

The scheme most often used, if the accessing part is a FPGA, is to program the fabric of the FPGA with an "artificial" short scan chain that can be used just for the duration of the flash programming stage.

JTAG Debugger | PowerDebug X51

The PowerDebug system reduces response times and improves upload/download speeds by placing a dedicated debug hardware

GDB for Nintendo Switch: How to Debug Homebrew Apps Like a Pro

Learn how to port and use GDB on Nintendo Switch for effective homebrew development debugging. Step-by-step guide with practical examples.

Catalyst 2960 Switch Debug Commands

Catalyst 2960 and 2960-S Switch Debug Commands This appendix describes the debug privileged EXEC commands that have been created or changed for use with the Catalyst 2960 and 2960-S

Leased Line Definition, Explanation, and Example

A leased line or sometimes called a dedicated line is a dedicated point-to-point link and fixed-bandwidth data connection.

Vivado Design Suite User Guide: Programming and Debugging (UG908)

Getting Started After successfully implementing your design, the next step is to run it in hardware by programming the FPGA device and debugging the design in-system. All of the necessary commands

UniFi SSH Commands Guide

SSH (Secure Shell) provides secure remote command-line access to UniFi access points, switches, gateways, and other devices. SSH is

JTAG and SWD debugging techniques: a field guide for

Practical guide to JTAG and SWD debugging techniques, from hardware to trace and scripting, plus Pcb design tips for reliable flashing and

## JTAG Advanced Capabilities and System Design

National provides products, software, and support for simple through complex embedded JTAG systems and can provide access and interconnect test to digital, analog, and differential nets.

## Contact Us

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